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Recruitment of the Junior Research Fellow for the Govt. of India sponsored Research Project

Applications are invited for the post of a Junior Research Fellow (JRF) at Department of Electronics & Communication Engineering, Faculty of Technology, Dharmsinh Desai University to work on a Research Project sponsored by Board of Research in Nuclear Science, Department of Atomic Energy, Govt. of India.

Details of the Project

Title	: Design & development of hardware module for the phase & power measurement for 64 channels of LHCD
Duration	: Two Years (2016-2018)
Principle Investigator	: Dr. Prarthan D. Mehta, Dept. of Electronics & Communication Engineering, Dharmsinh Desai University
Co-Investigator	: Dr. Rizwan Alad, Dept. of Electronics & Communication Engineering, Dharmsinh Desai University
Project Collaborator	: Dr. P. K. Sharma, Scientist-SG, Institute of Plasma Research

Details of the Post

Post	: Junior Research Fellow (01 No.)
Monthly Emoluments	: Up to Rs. 25000/- + House Rent Allowance as per applicable rules
Qualifications	: B.E./B.Tech./M.E./M.Tech. Electronics & Communication or equivalent Experience in the relevant field will be considered for the scrutiny
Duration	: The initial appointment will be for one year, with the possibility of extension based on the performance.

Interested candidates should mail their complete CV by E-mail to mehtaparthan.ec@ddu.ac.in latest by September 03, 2016. Applicants must mention **Junior Research Fellow** in the subject filed of the E-mail.

The shortlisted candidates will be intimated regarding the schedule of the further process through E-mail.